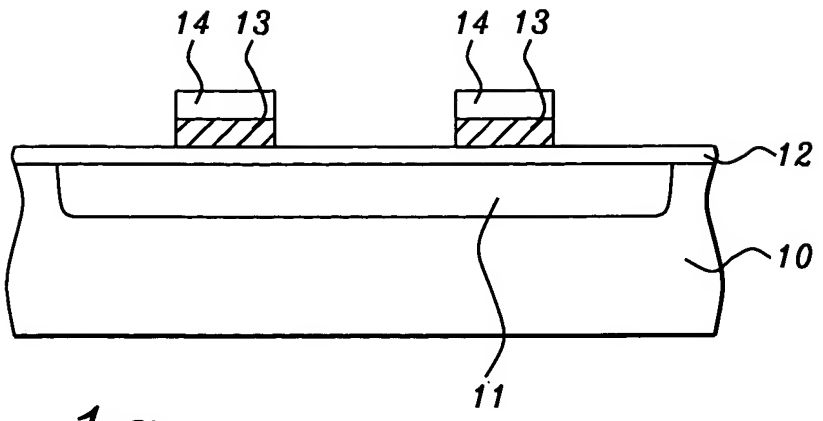


FIG. 1a

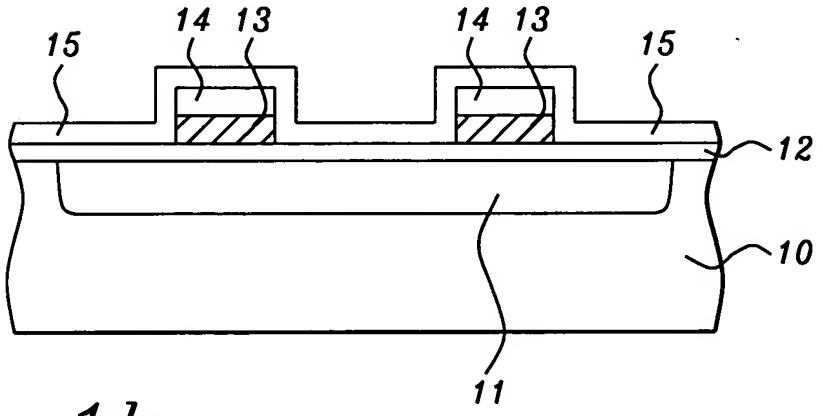


FIG. 1b

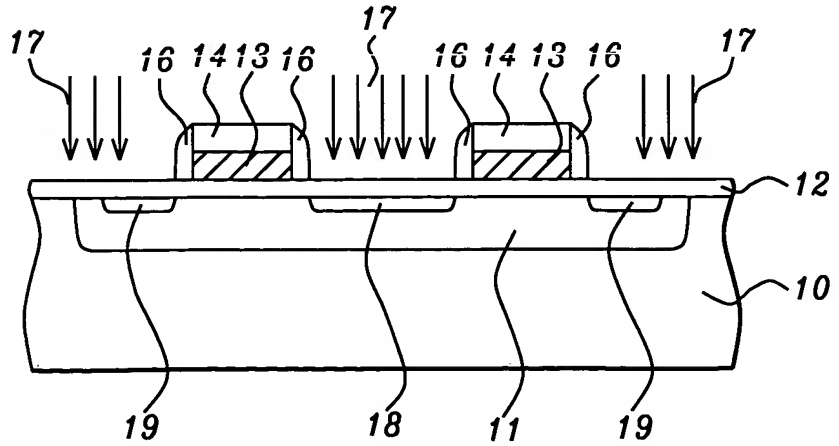


FIG. 1c

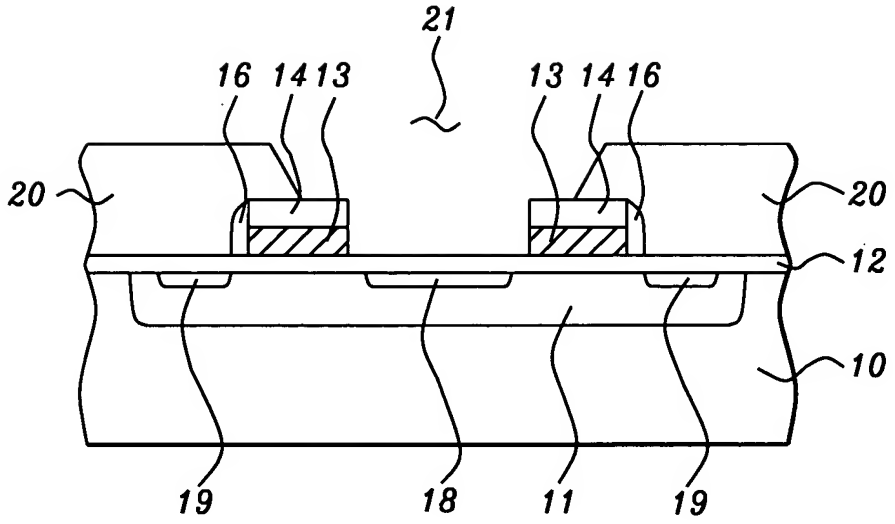

21
16 14 13
13 14 16
20
20
12
10
19
18
11
19

FIG. 1d

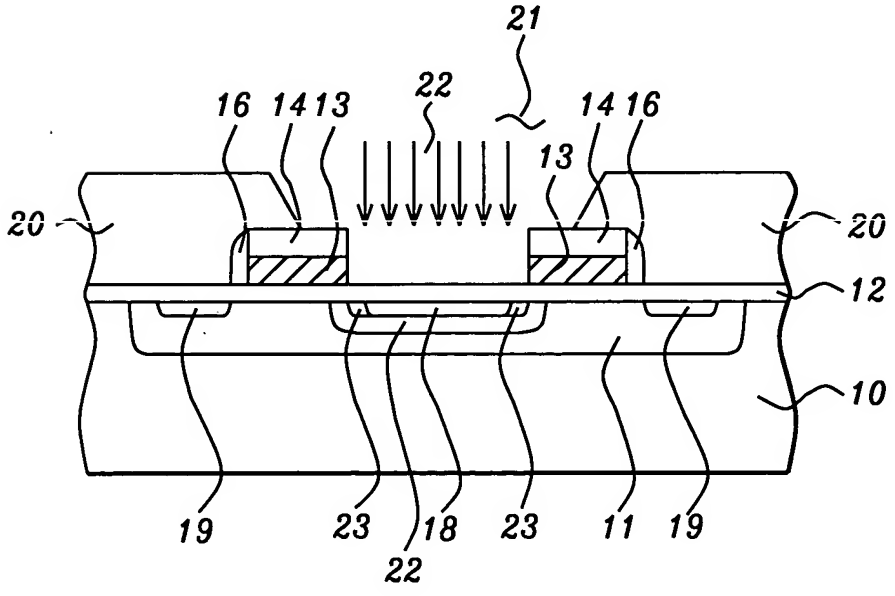
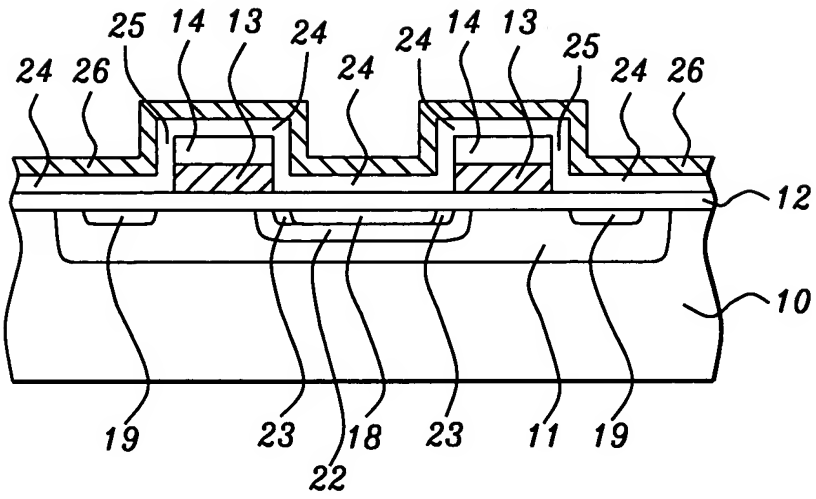

21
22
16 14 13
14 16
13
20
20
12
10
19
23
18
23
11
19
22

FIG. 1e


25
14
13
24
24
24
14
13
25
24
26
24
26
12
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22
18
23
11
19

FIG. 1f

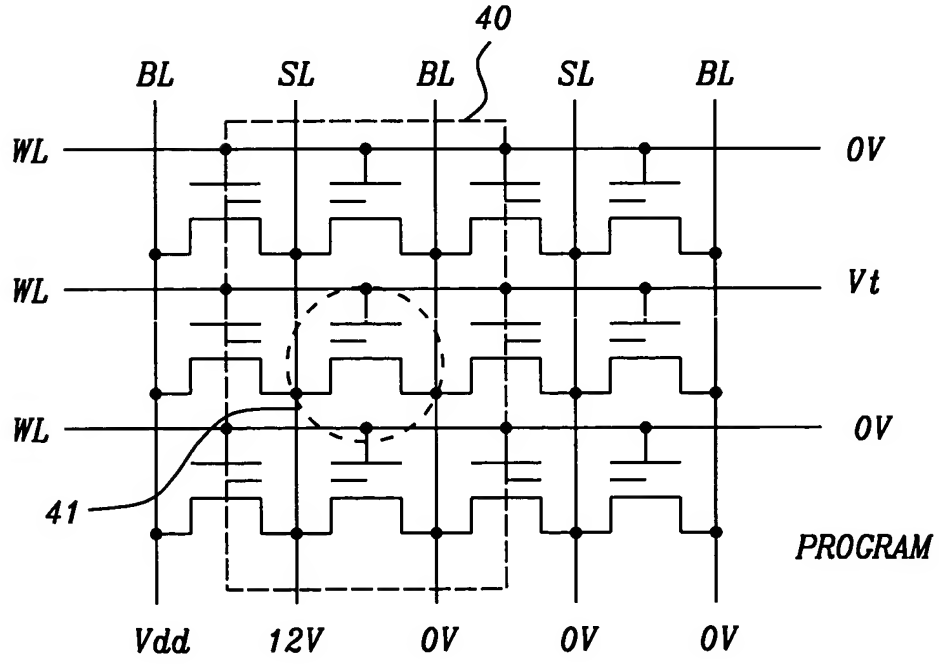
40
BL
SL
BL
SL
BL
WL
WL
WL
41
0V
Vt
0V
PROGRAM
Vdd
12V
0V
0V
0V

FIG. 2a

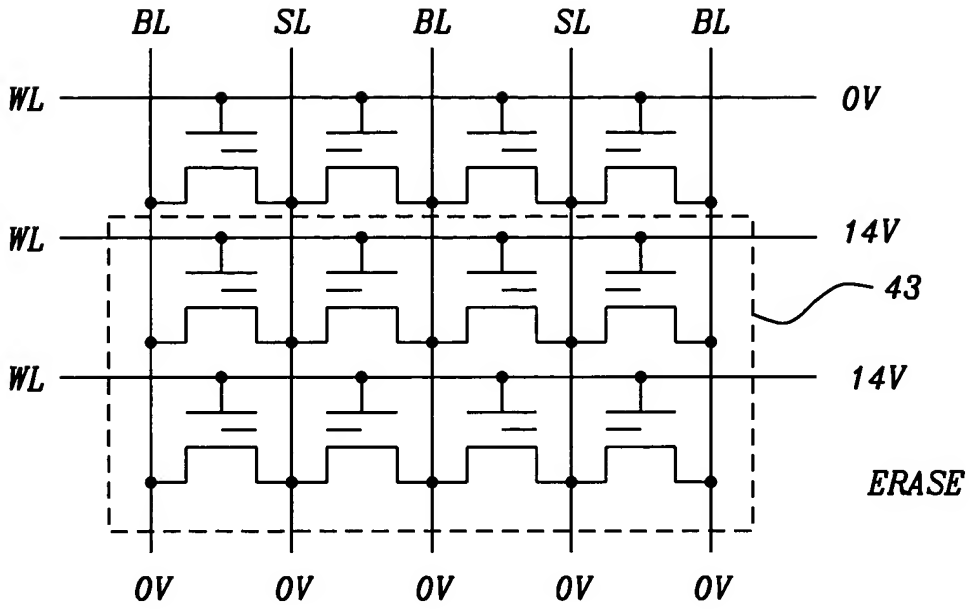
BL
SL
BL
SL
BL
WL
WL
WL
0V
14V
14V
43
ERASE
0V
0V
0V
0V
0V

FIG. 2b

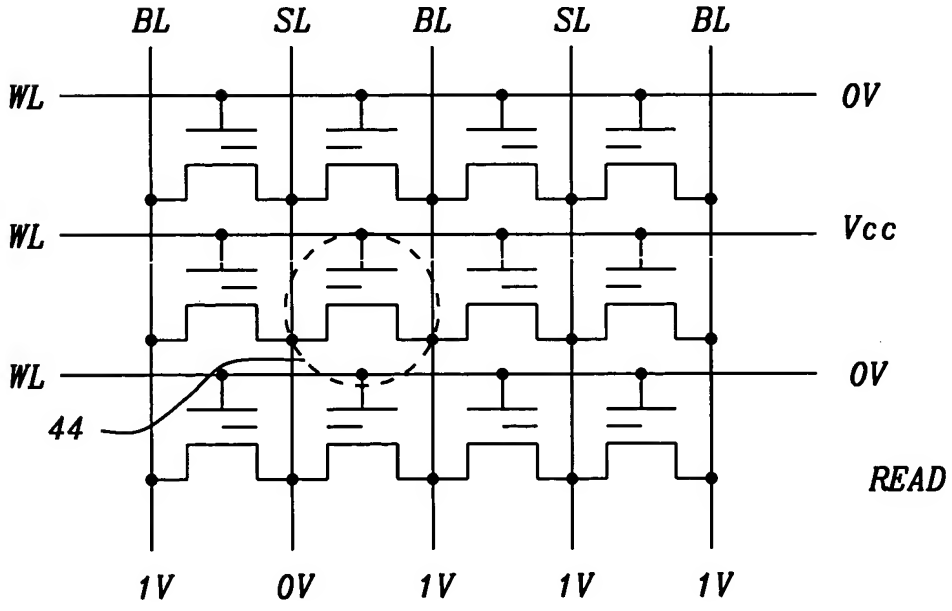
BL
SL
BL
SL
BL
WL
WL
WL
44
0V
Vcc
0V
READ
1V
0V
1V
1V
1V

FIG. 2c

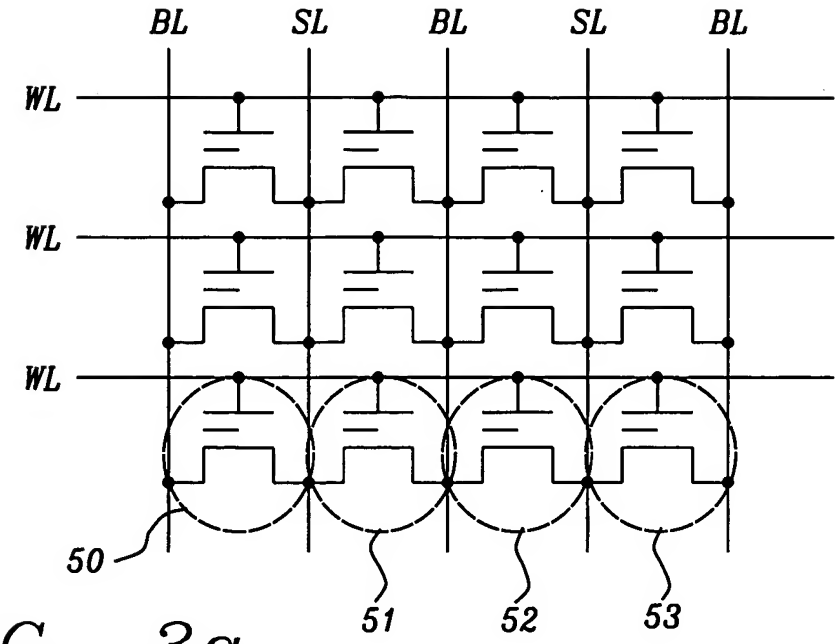

FIG. 3a

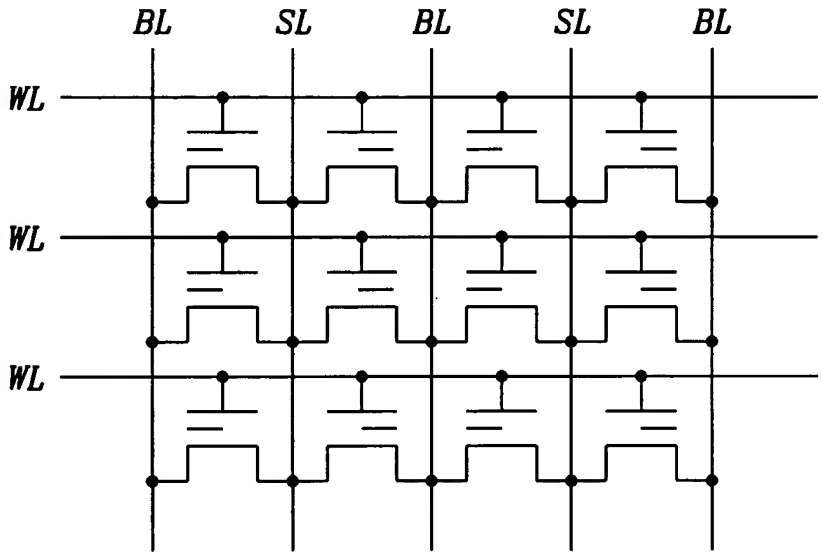

FIG. 3b

	WL	BL	SL
Program	Vt/Gnd	Gnd	12V
Erase	14V	Gnd	Gnd
Read	Vcc	1V	Gnd

FIG. 4